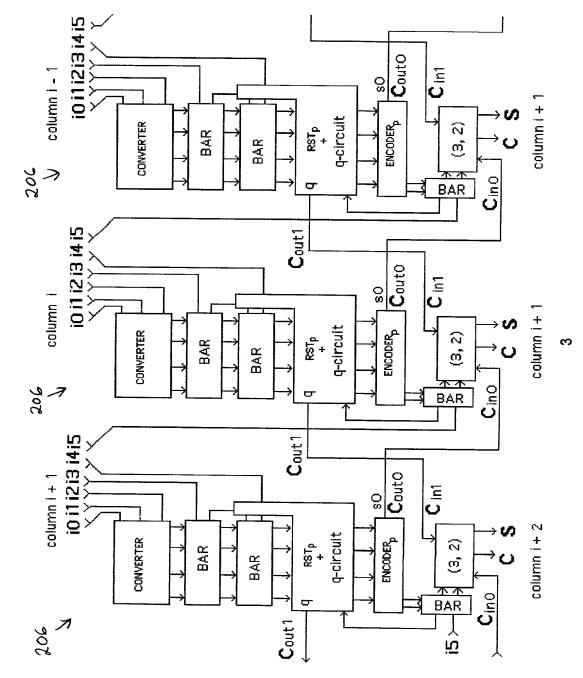
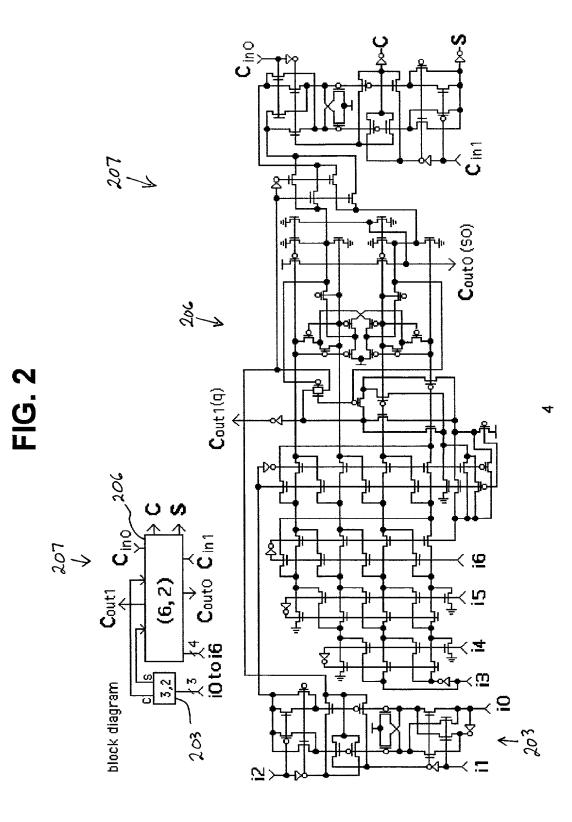
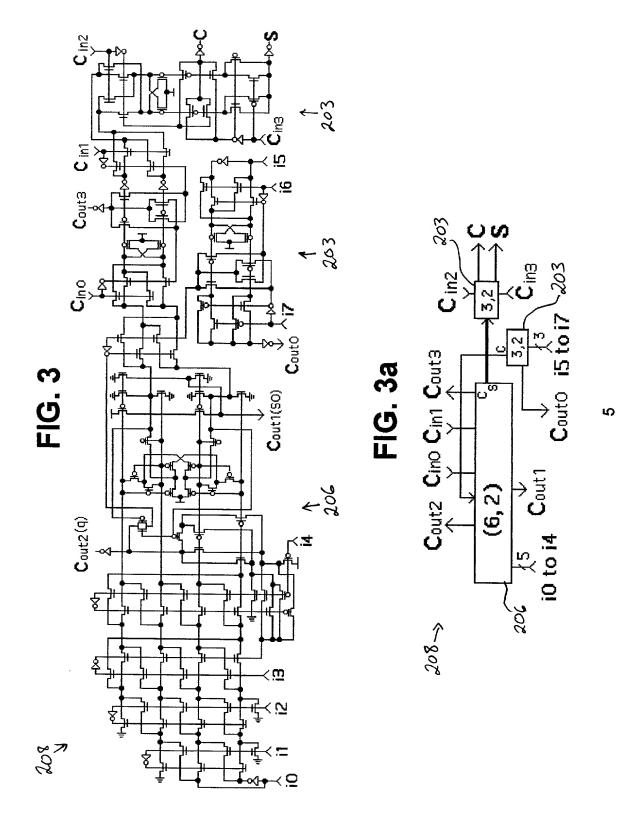


FIG. 1b







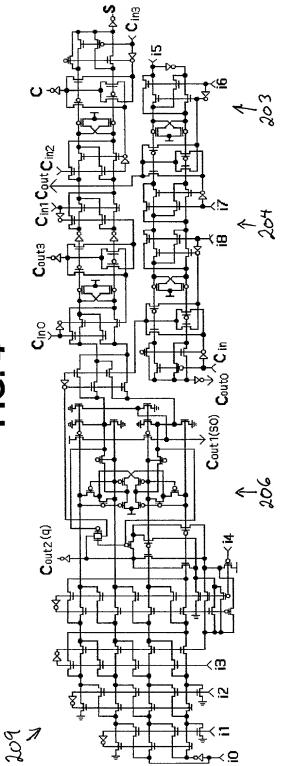
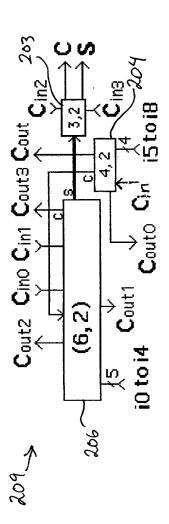
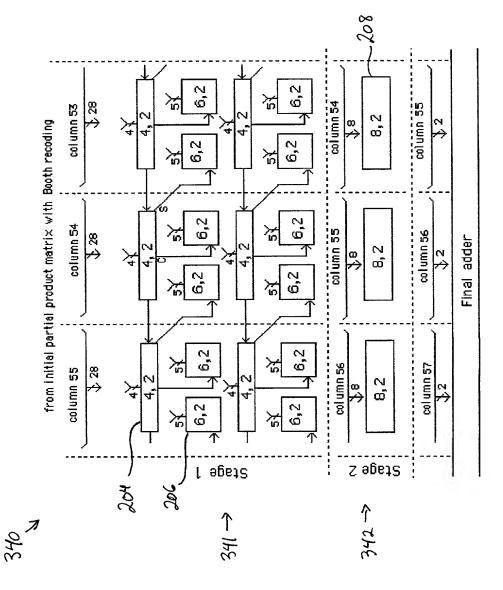
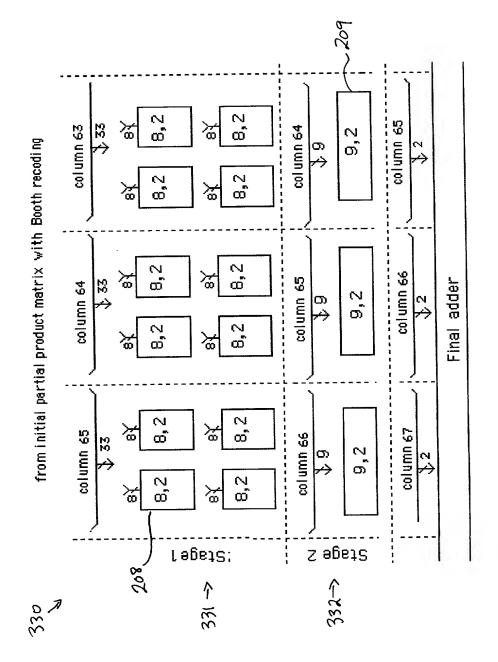
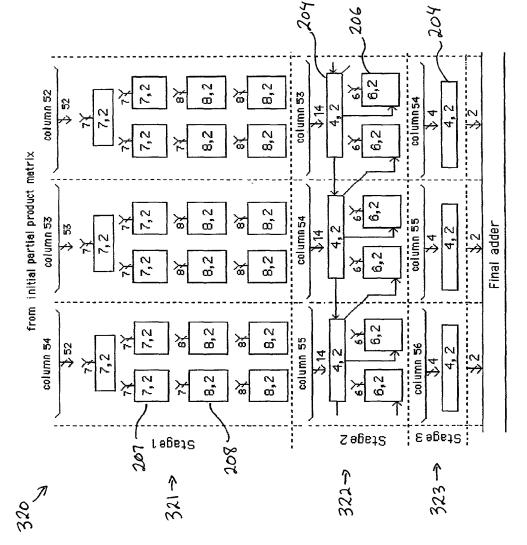


FIG. 4a

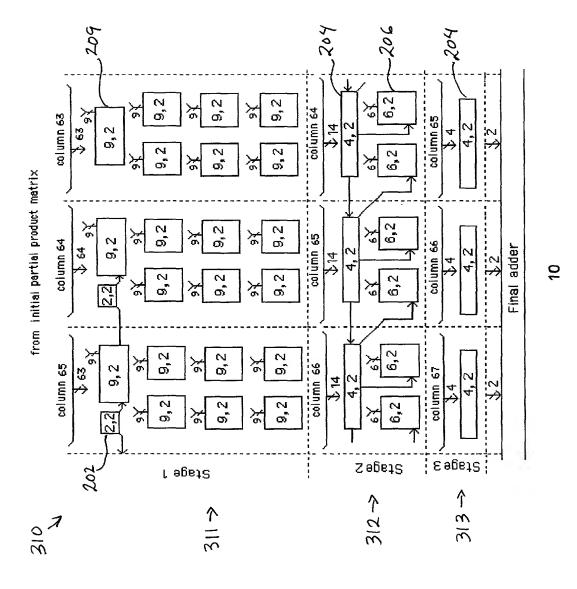


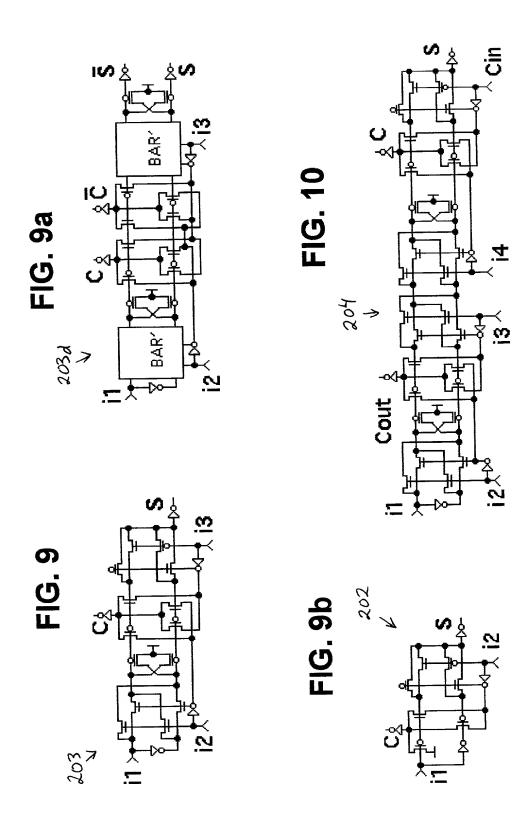






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g→

n-type

:. :: : 0x

×3:

x2:

:: :: .. 0x

x2:

x ← 30

× 14

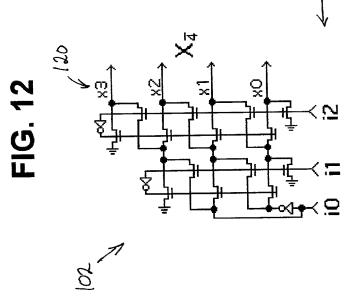
p-type

2

×

X=1

0 = X



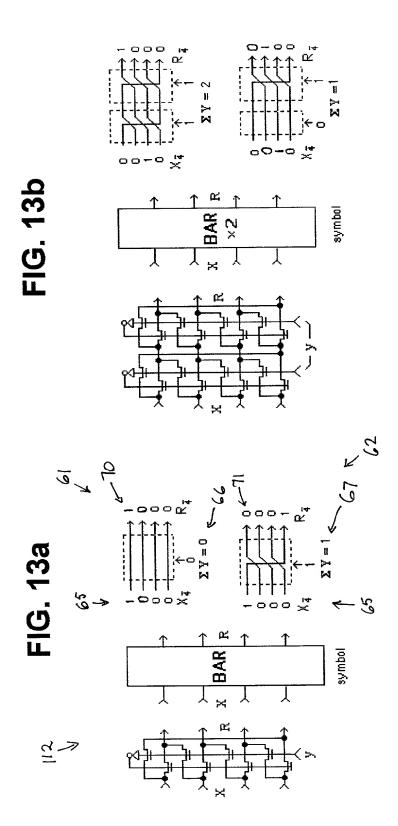
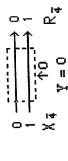
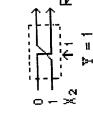


FIG. 13c

+ 0 = [¥ 0 = [¥

FIG. 13d

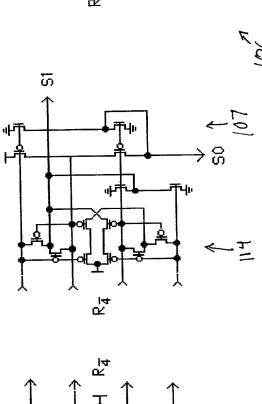




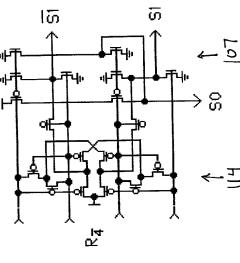
BAR



15

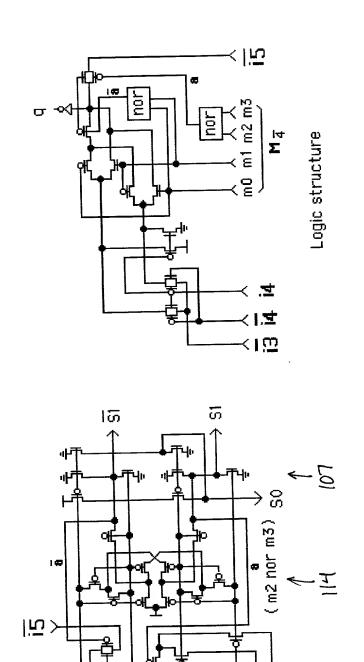


다. |4



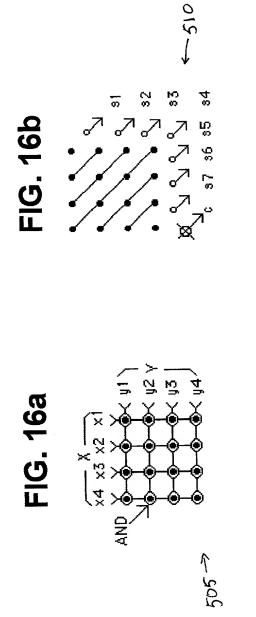
က

FIG. 15



돝

Σ |-4 <u> (iii</u>



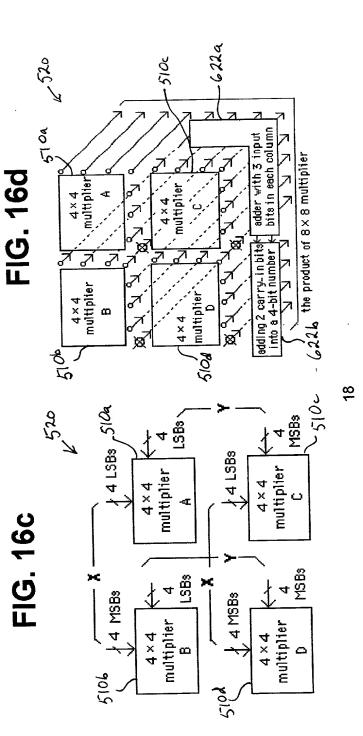
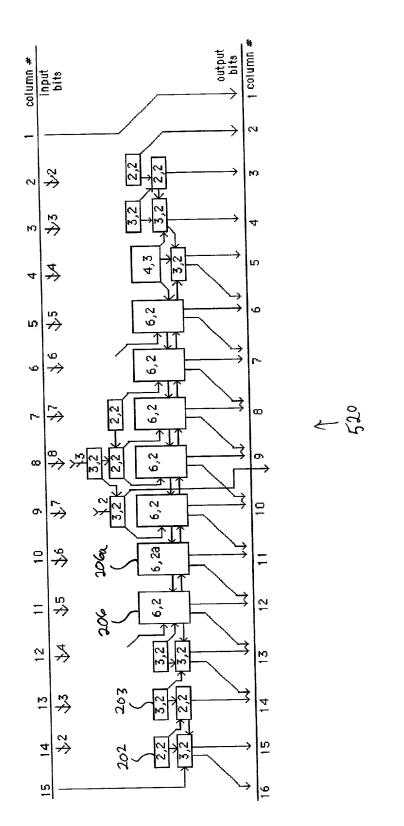


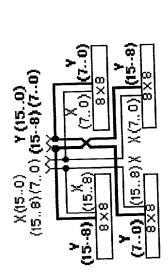
FIG. 17



31.24 23.16 15.8 7.0 0 21.24 23.16 11.5 8 7 0 21.24 23.16 15.8 7 0 24.23 16 15.8 7 0 15.8 7 FIG. 18c 15.8 7.0 3 01 : A1 7.1 15.8 7.0 8 3 FIG. 18b 7.4 3.0 7.4 3.0 C3 11 A3 11 FIG. 18a 15.12 15.12 15.12 16.23 17.12 17.12 17.13 17

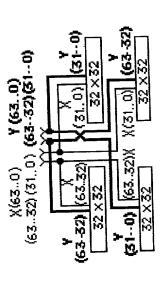
FIG. 19a

FIG. 19b



(31..16)(15..0) (31..16)(15..0) (31..16)(15..0) (31..16)(15..0) (31..16)(15..0) (15..0) (15..0) (31..16) (15..0) (15..0) (31..16) (31..16)

FIG. 19c





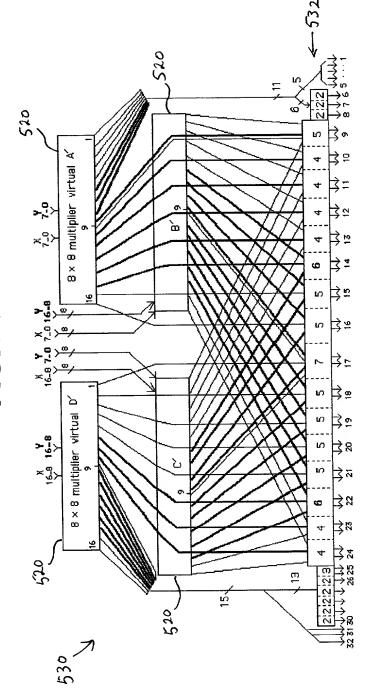
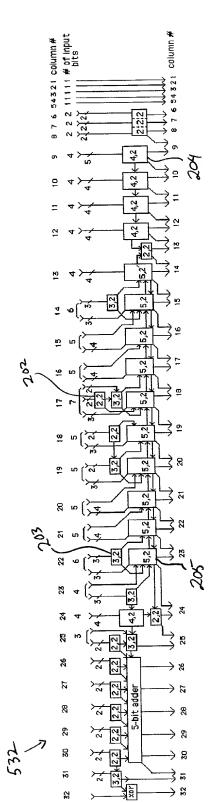
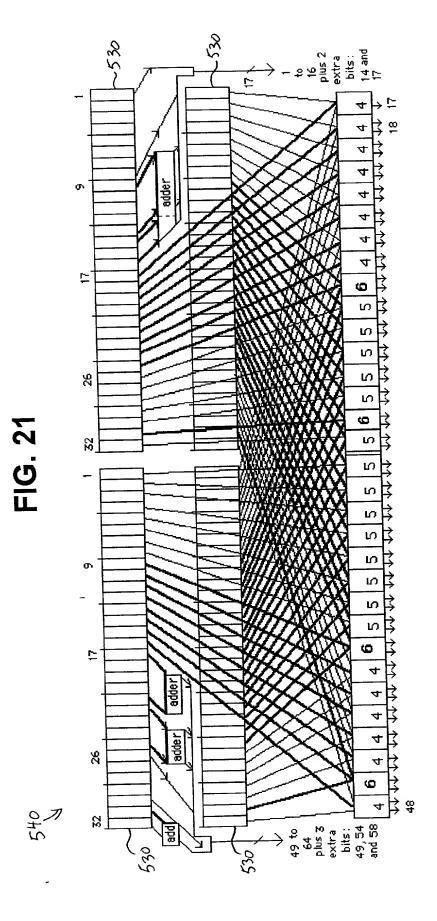
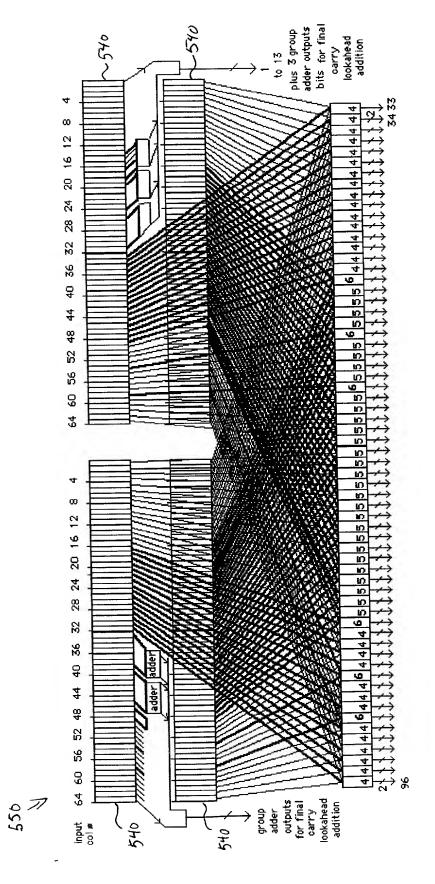


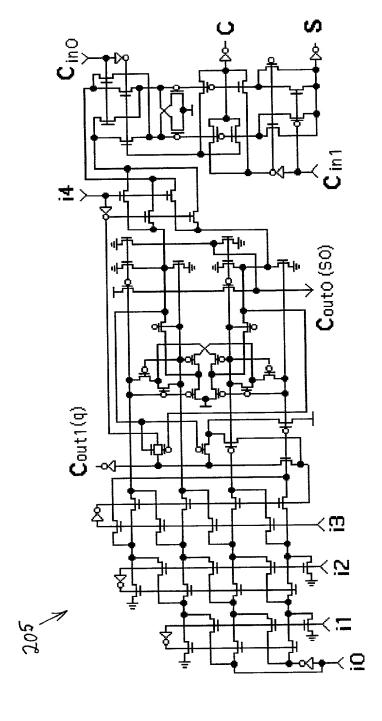
FIG. 20a

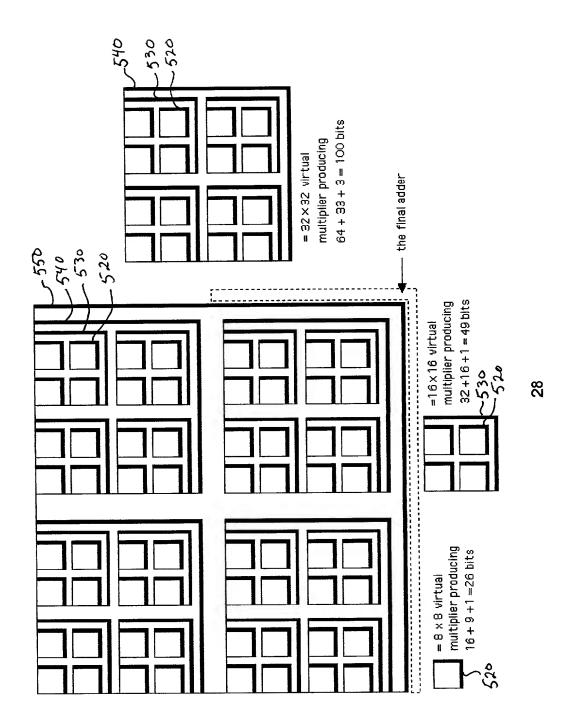






C in 1 206a Ç di C ¢ Cout1 Cino , Couto < Cin1 iO to i5 block diagram Cout1 ←





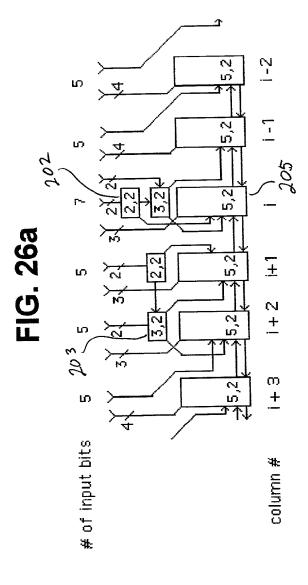


FIG. 26b

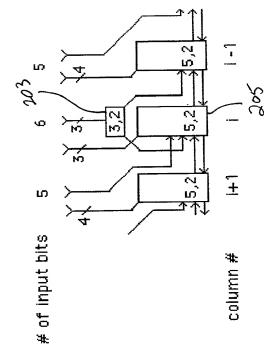
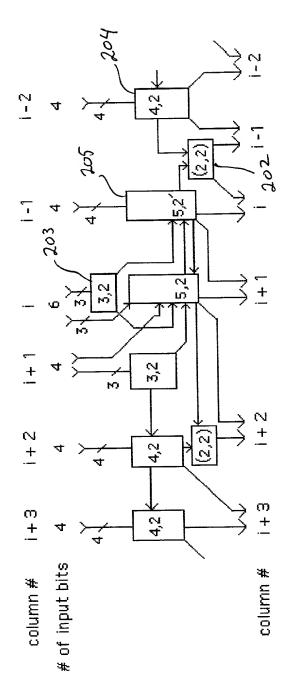
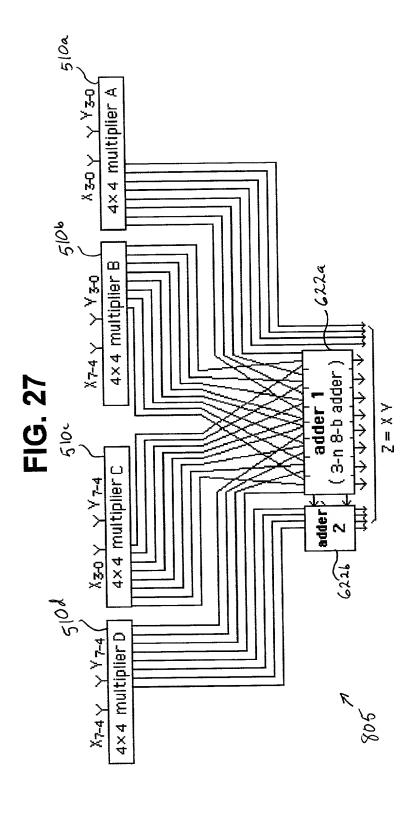
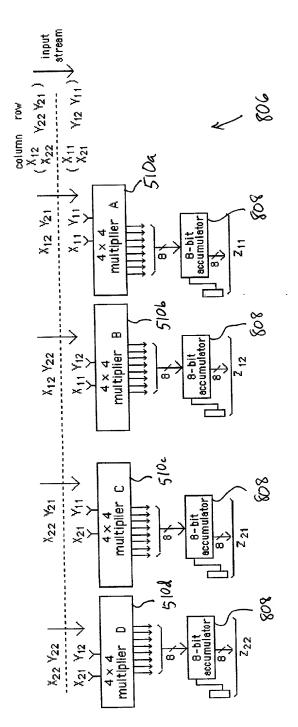
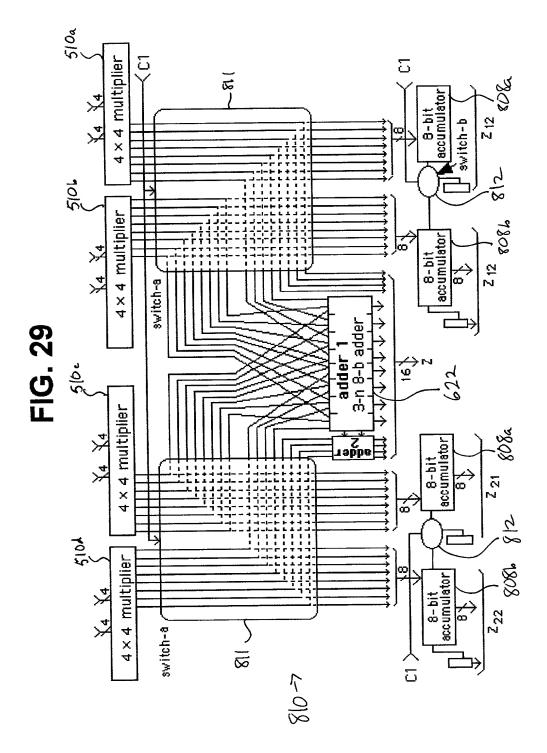


FIG. 26c

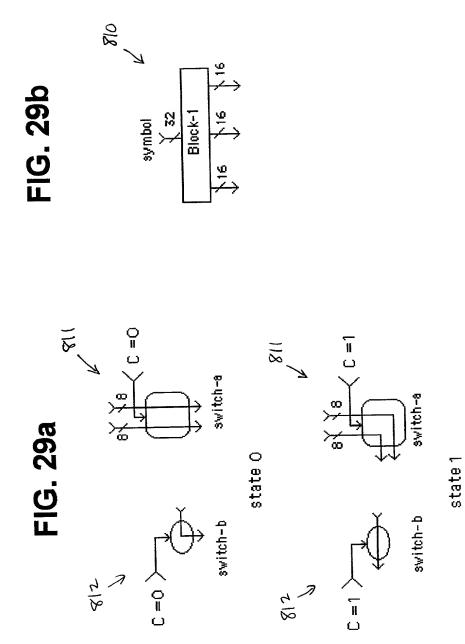


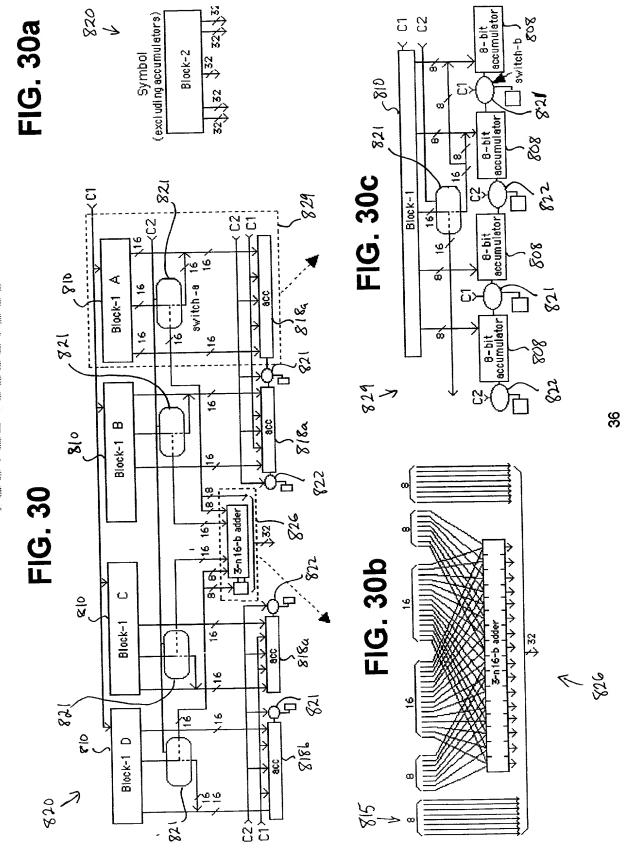






34





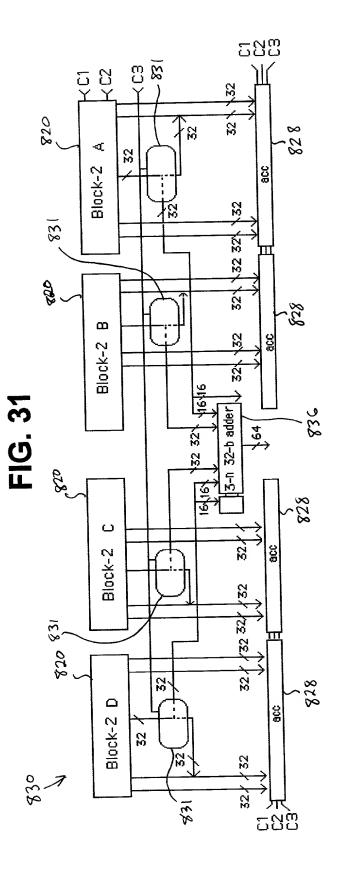


FIG. 31a 830

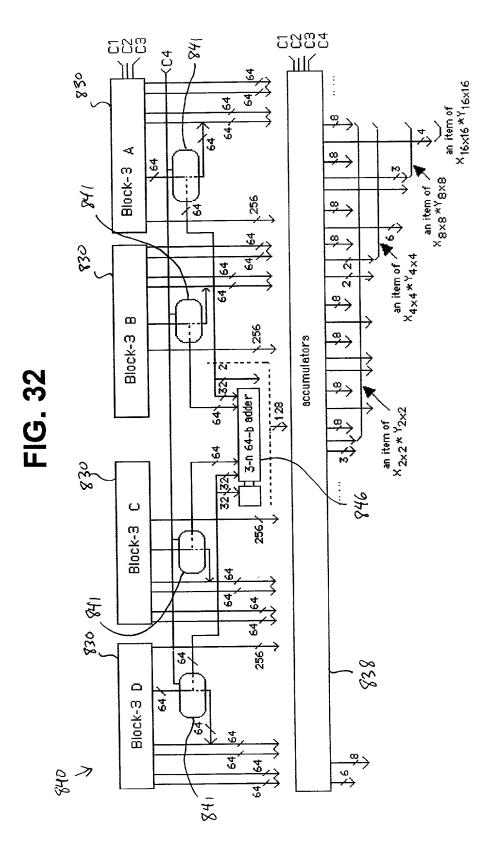
Symbol

[excluding accumulators]

Block-5

Ed \$\frac{1}{2} \frac{1}{2} \frac

37



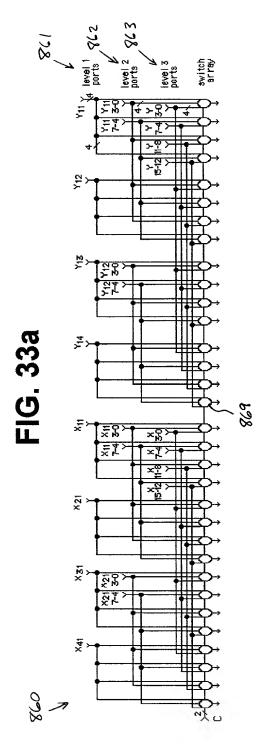


FIG. 33b

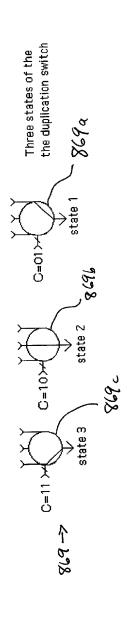
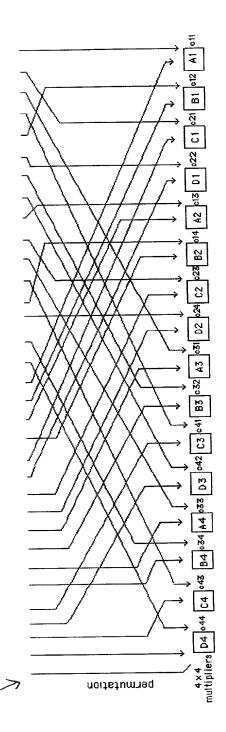


FIG. 33c

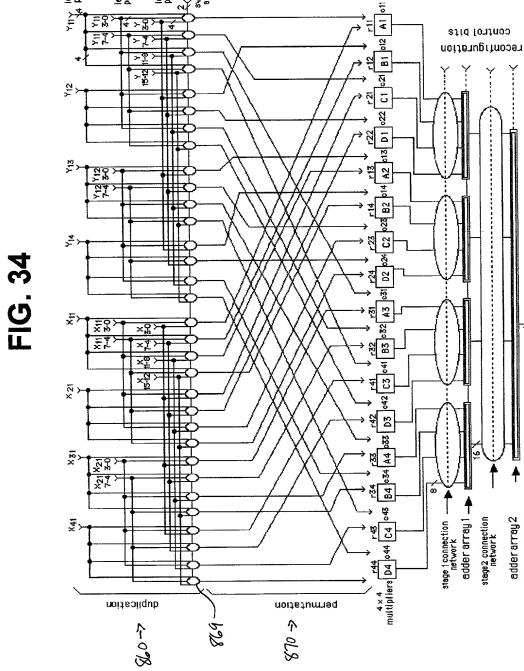


YIIY level 1

level 2 ports

₹≻**%**≻

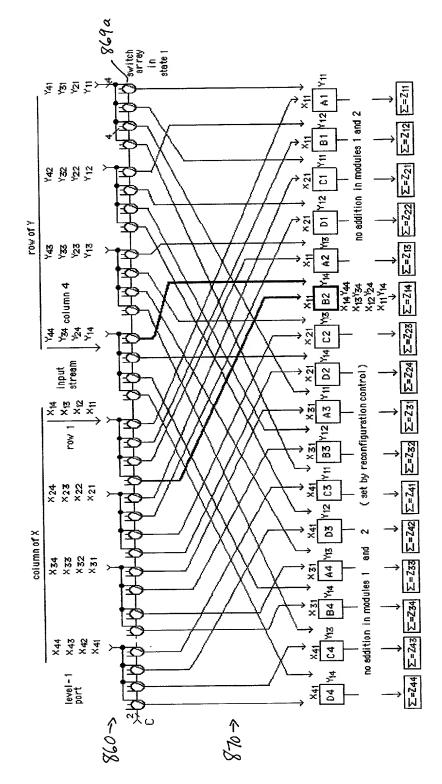
level 3 ports 2<0 switch srray



(the final addition/accumulation)

adderarray2

control bits



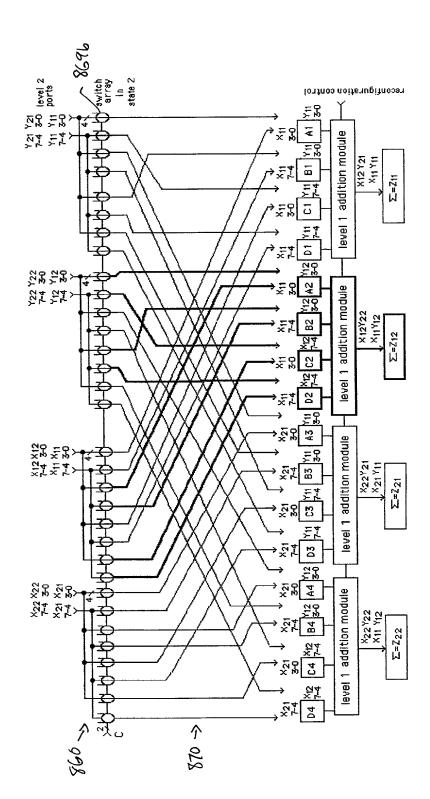


FIG. 36a

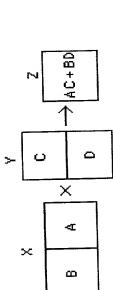
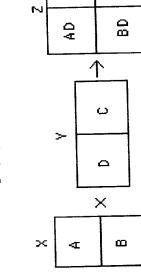


FIG. 36b



AC

BC

FIG. 36c

